

OCT 05 2004

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: Emons, M, et al.  
 Application No.: 09/829793 Conf.: 1893  
 Date Filed: 04/10/2001

Docket No.: NL000215  
 Art Unit: 2186  
 Examiner: Kim, Hong Chong

Title: DATA PROCESSING CIRCUIT WITH A CACHE MEMORY AND APPARATUS  
 CONTAINING SUCH A CIRCUIT

Commissioner for Patents  
 P.O. Box 1450  
 Arlington, VA 22313-1450

**TRANSMITTAL LETTER**

Sir:

There are a total of 6 pages in this submission which includes this transmittal letter, as well as the documents listed below:

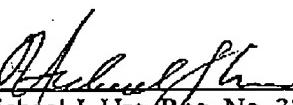
- Response to Office action dated 08-SEP-2004  
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The Commissioner is hereby requested and authorized pursuant to 37 CFR §1.136(a)(3), to treat any concurrent or future reply in this application requiring a petition for extension of time for its timely submission, as incorporating a petition for extension of time for the appropriate length of time. Please charge any additional fees which may now or in the future be required in this application, including extension of time fees, but excluding the issue fee unless explicitly requested to do so, and credit any overpayment, to Deposit Account No. 14-1270.

Date: 10/5/04

Respectfully submitted,

By

  
 Michael J. Ure, Reg. No. 33,089  
 (408) 474-9077  
 Philips Electronics North America Corp.  
 1109 McKay Drive, M/S 41-SJ  
 San Jose, CA 95131

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(Date) 10/05/04(Signature)  
(Name)

Daniel L. Michalek

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